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SC91051A  
PATENT  
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): Supamas Sirichotiyakul  
APPLN. NO.: 09/380,854  
FILED: May 30, 2000  
TITLE: METHOD FOR ANALYZING INTEGRATED CIRCUITS AND APPARATUS  
THEREFOR

GROUP ART UNIT: 2123  
EXAMINER: Kandasamy Thangavelu

Certificate of Transmission under 37 CFR 1.8

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the Patent and Trademark Office.

on

Mail Date

Pat Thomas

**NOTICE OF APPEAL FROM THE EXAMINER TO THE  
BOARD OF PATENT APPEALS AND INTERFERENCES**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

SIR:

In accordance with 37 CFR 1.191, applicant hereby appeals to the Board of Patent Appeals and Interferences from the last decision of the examiner. The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. 503079, Freescale Semiconductor, Inc. This Notice is submitted in duplicate.

Respectfully submitted,  
Supamas Sirichotiyakul

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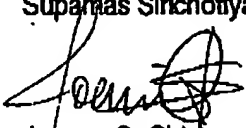
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